



Attorney Docket No.: 60188-084
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	: Customer Number: 20277
Shigeki FURUYA, et al.	: Confirmation Number: 1398
Serial No.: 09/922,671	: Group Art Unit: 2815
Filed: August 07, 2001	: Examiner: Matthew E. WARREN
For: CMOS BASIC CELL AND METHOD FOR FABRICATING SEMICONDUCTOR INTEGRATED CIRCUIT USING THE SAME	

AMENDMENT UNDER 37 C.F.R. § 1.111

Mail Stop Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed December 5, 2003, having a shortened statutory period for response set to expire on March 5, 2004, a one month extension of time up to and including April 5, 2004 being filed concurrently herewith, please amend the above-identified application as follows:

03/12/2004 SZEWDIE1 00000125 500417 09922671

01 FC:1201	86.00 DA
02 FC:1202	90.00 DA